

PW080N10DSL

Perfect MOS5 N-MOSFET 100V, 6.7mΩ, 80A



重庆平伟实业股份有限公司

Features

- Uses PingWei advanced PerfectMOS5 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g \times R_{DS(on)}$ product(FOM)
- Excellent Low Ciss
- Qualified according to JEDEC criteria

Benefits

- High robustness and reliability
- Increases maximum current capability
- Low power loss, high power density
- Easy paralleling

Applications

- Synchronous Rectification for AC/DC Quick Charger
- Battery management
- UPS (Uninterruptible Power Supplies)

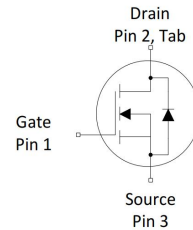
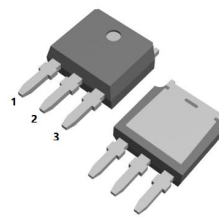


100% DVDS Tested
100% Avalanche Tested

Product Summary

V_{DS}	100V
$R_{DS(on)@10V}$ typ	6.7mΩ
$R_{DS(on)@4.5V}$ typ	9.5mΩ
I_D	80A

TO-251-3L



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PW080N10DSL	080N10DSL	TO-251-3L	Tube	N/A	N/A	80pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	100	V
Continuous drain current	I_D	94	A
$T_C = 25^\circ\text{C}$ (Silicon limit)		80	
$T_C = 25^\circ\text{C}$ (Package limit)		59	
$T_C = 100^\circ\text{C}$ (Silicon limit)		10	
$T_a = 25^\circ\text{C}$			
Pulsed drain current ($T_C = 25^\circ\text{C}$, $t_p = 100\mu\text{s}$)	$I_{D\ pulse}$	320	A
Avalanche energy, single pulse ($L=0.5\text{mH}$, $V_{ds}=50\text{V}$)	E_{AS}	64	mJ
Gate-Source voltage	V_{GS}	± 20	V
Power dissipation	P_{tot}	105	W
$T_C = 25^\circ\text{C}$		1.1	
$T_a = 25^\circ\text{C}$			
Operating junction and storage temperature	T_j, T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	T_{sold}	260	$^\circ\text{C}$



Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case.	RthJC	-	0.75	1.2	°C/W	-
Thermal resistance, junction - ambient(min. footprint)	RthJA	-	-	113	°C/W	-

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	100	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	1.2	-	2.4	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	I_{DSS}	-	0.02	1	μA	$V_{DS}=100V, V_{GS}=0V$ $T_j=25^\circ C$ $T_j=150^\circ C$
Gate-source leakage current	I_{GSS}	-	± 10	± 100	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	6.7	8.0	mΩ	$V_{GS}=10V, I_D=40A$
		-	9.5	12.4	mΩ	$V_{GS}=4.5V, I_D=40A$
Transconductance	g_{fs}	-	59	-	S	$V_{DS}=5V, I_D=40A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	2590	-	pF	$V_{GS}=0V, V_{DS}=50V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	691	-		
Reverse Transfer Capacitance	C_{rss}	-	31	-		
Gate Total Charge	Q_G	-	44	-	nC	$V_{DS}=50V, I_D=40A,$ $V_{GS}=10V$
Gate-Source charge	Q_{gs}	-	13	-		
Gate-Drain charge	Q_{gd}	-	8	-		
Turn-on delay time	$t_{d(on)}$	-	15	-	ns	$V_{GS}=10V, V_{DD}=50V,$ $R_{G_ext}=1.6\Omega, I_D=13A$
Rise time	t_r	-	29	-		
Turn-off delay time	$t_{d(off)}$	-	31	-		
Fall time	t_f	-	39	-		
Gate resistance	R_G	-	1.4	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$



Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.9	1.2	V	$V_{GS}=0V, I_{SD}=40A$
Body Diode Continuous Forward Current	I_S	-	-	80	A	TC = 25°C
Body Diode Pulsed Current	I_S pulse	-	-	320	A	TC = 25°C
Body Diode Reverse Recovery Time	t_{rr}	-	41	-	ns	$V_R=50V, I_F=40A,$ $diF/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	Q_{rr}	-	45	-	nC	



Typical Performance Characteristics

Fig 1: Output Characteristics

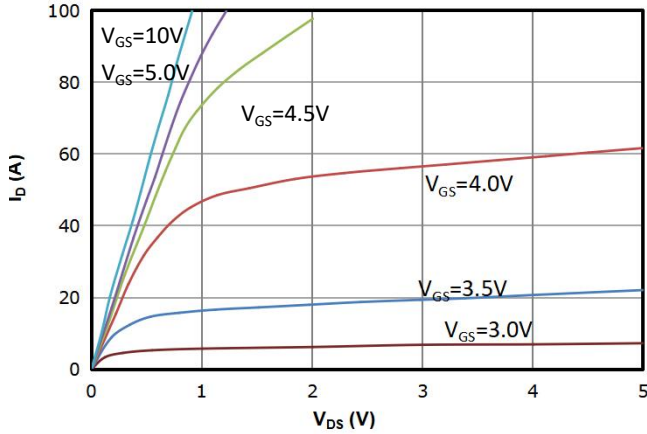


Fig 2: Transfer Characteristics

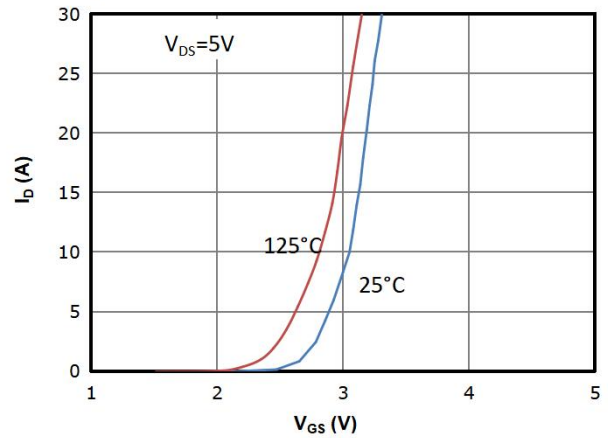


Fig 3: $R_{DS(on)}$ vs Drain Current and Gate Voltage

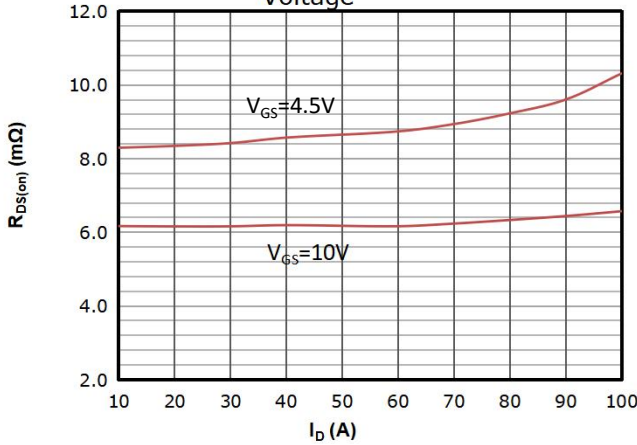


Fig 4: $R_{DS(on)}$ vs Gate Voltage

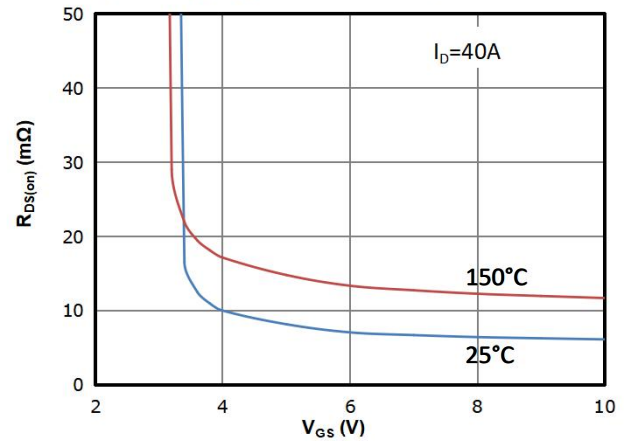


Fig 5: $R_{DS(on)}$ vs. Temperature

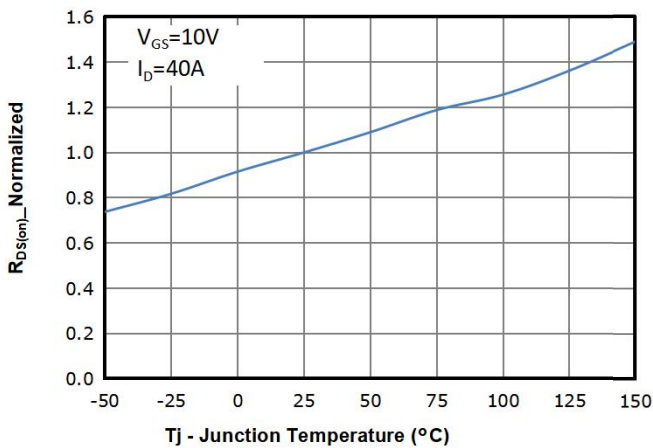


Fig 6: $V_{GS(th)}$ vs. Temperature

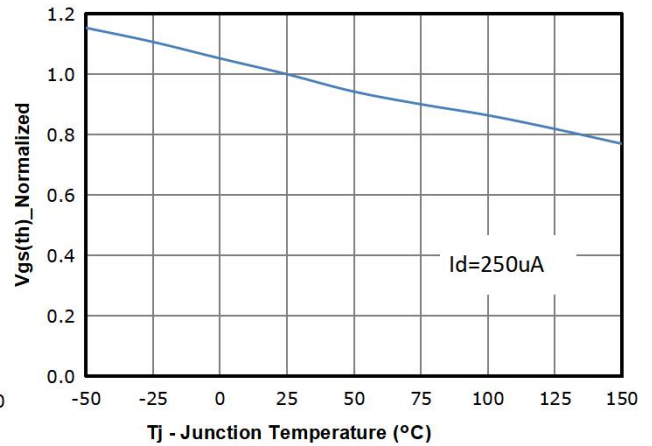




Fig 7: BVdss vs. Temperature

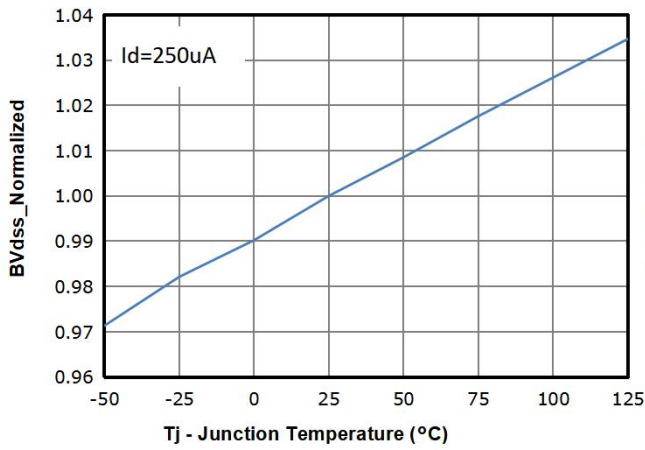


Fig 8: Capacitance Characteristics

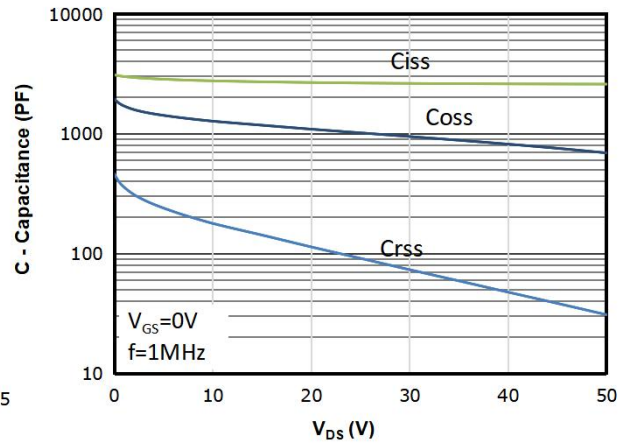


Fig 9: Gate Charge Characteristics

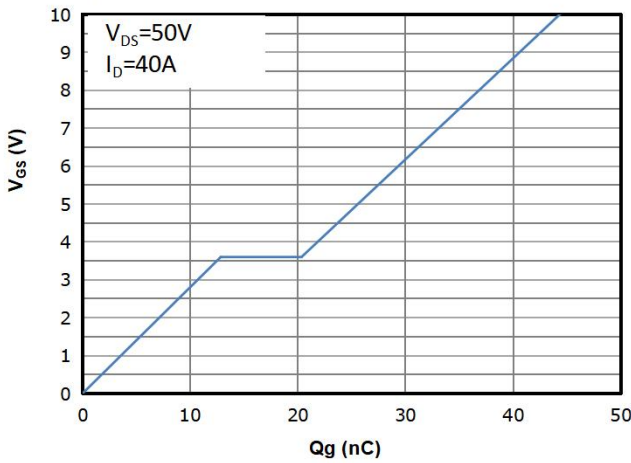


Fig 10: Body-diode Forward Characteristics

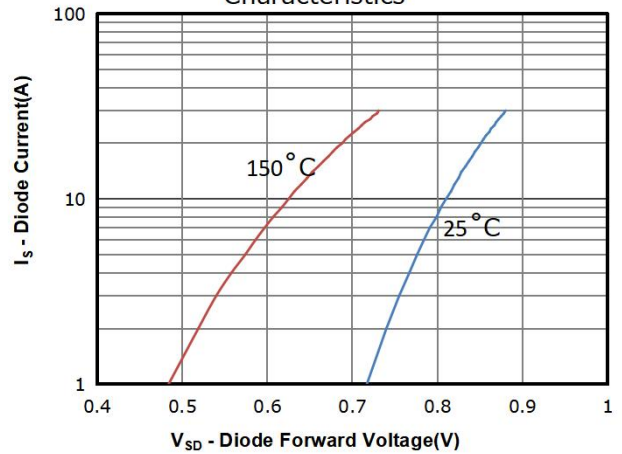


Fig 11: Power Dissipation

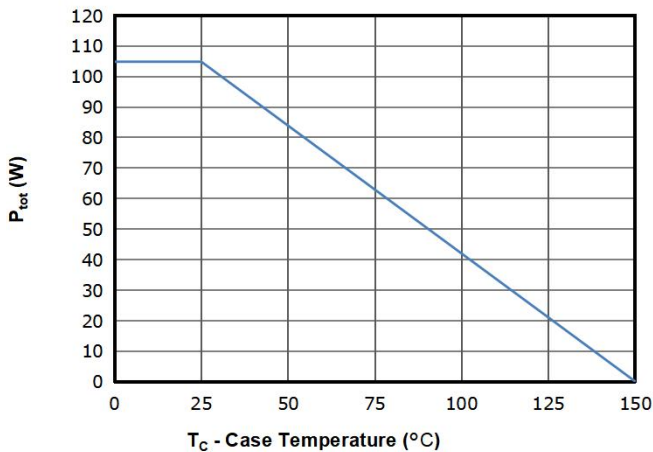


Fig 12: Drain Current Derating

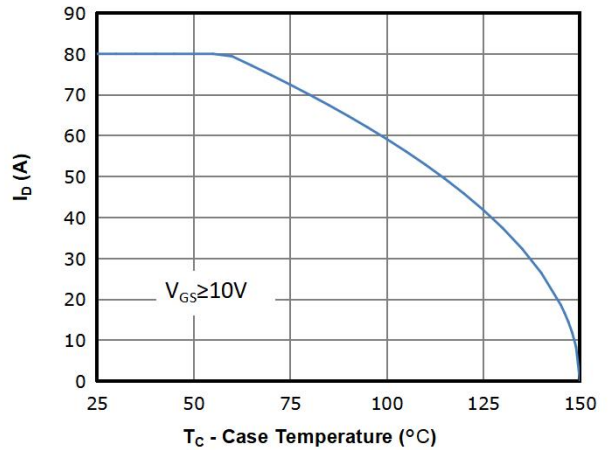




Fig 13: Safe Operating Area

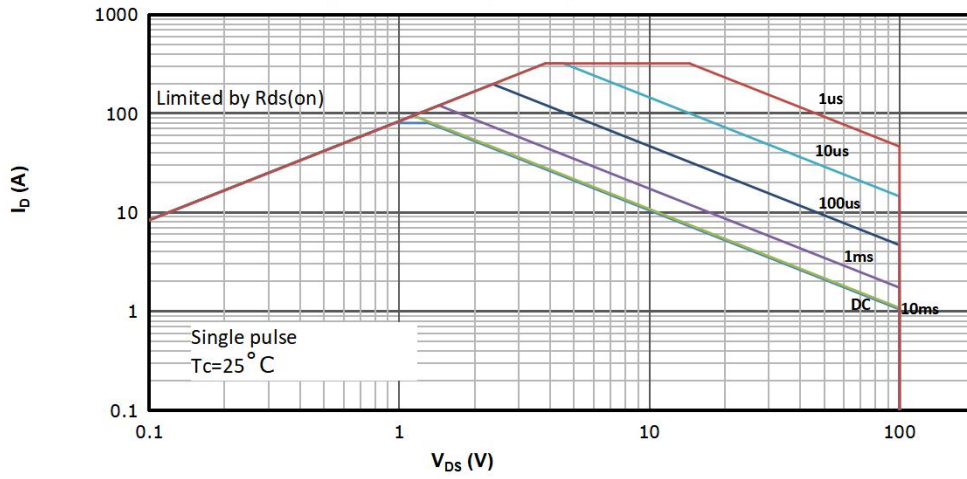
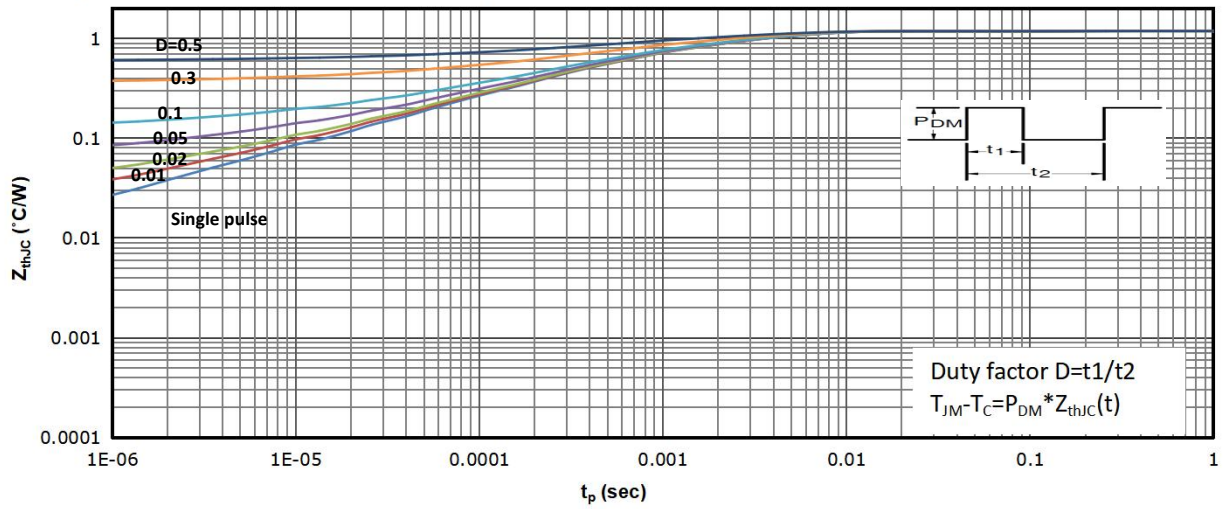


Fig 14: Max. Transient Thermal Impedance

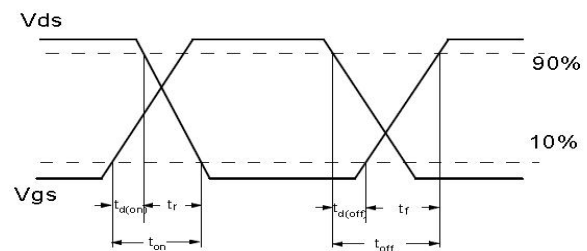


Test Circuit & Waveform

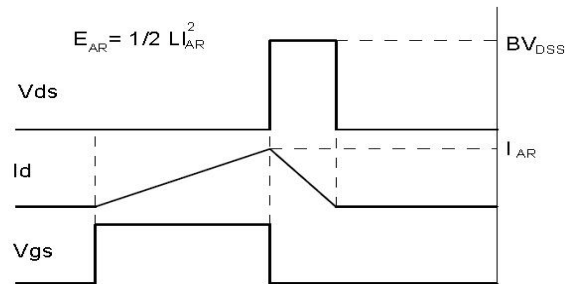
Gate Charge Test Circuit & Waveform



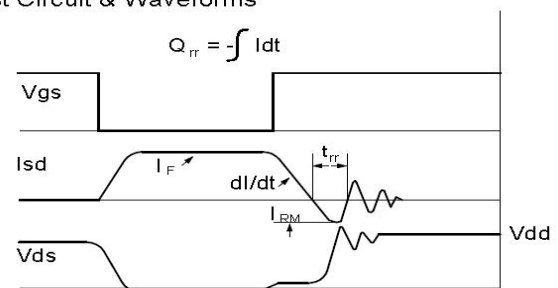
Resistive Switching Test Circuit & Waveforms



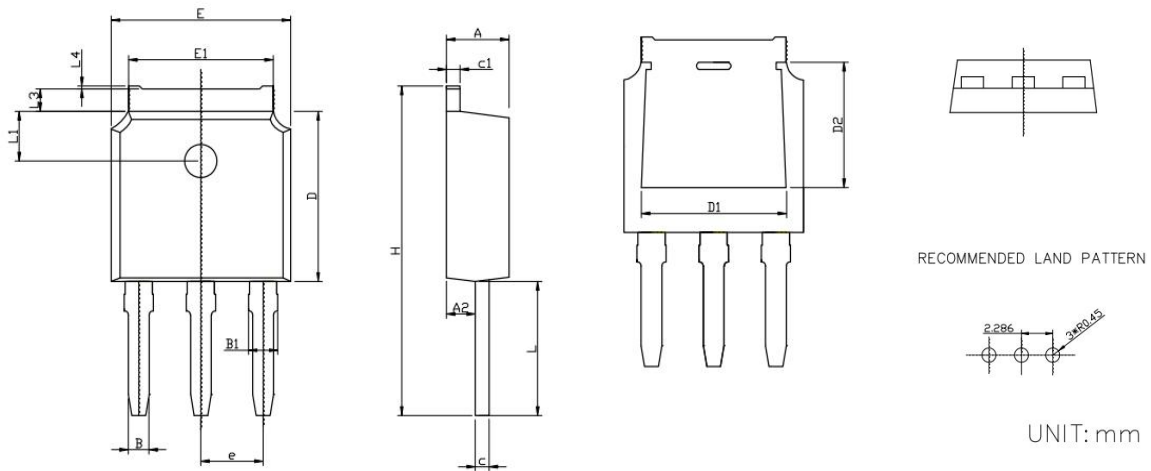
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: TO-251-3L



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.15	2.45	0.085	0.096
A1	0.91	1.22	0.036	0.048
B	0.66	0.86	0.026	0.034
B1	0.93	1.23	0.037	0.048
C	0.40	0.60	0.016	0.024
C1	0.40	0.60	0.016	0.024
D	5.95	6.25	0.234	0.246
D1	4.80		0.189	
D2	3.80		0.150	
E	6.45	6.75	0.254	0.266
E1	5.12	5.52	0.202	0.217
L	4.50	5.10	0.177	0.201
L1	1.58	1.98	0.062	0.078
L3	0.70	1.00	0.028	0.039
L4	0.00	0.20	0.000	0.008
H	11.50	12.10	0.453	0.476
e	2.286		0.090	



Revision History

Revision	Date	Major changes
1.0	2023/2/8	Release of Formal Version.

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